

# USER MANUAL

WSEN-PDMS

25131308xxx05

VERSION 1.0

NOVEMBER 27, 2025

**WÜRTH ELEKTRONIK** MORE THAN YOU EXPECT

## Revision history

Manual version	Notes	Date
1.0	<ul style="list-style-type: none"><li>Initial release of the manual</li></ul>	November 2025

## Abbreviations

Abbreviation	Description
ASIC	Application Specific Integrated Circuit
BFSL	Best Fit Straight Line
ESD	Electrostatic Discharge
EEPROM	Electrically erasable programmable read-only memory
FSS	Full Scale Span
HBM	Human Body Model
HVAC	Heating, ventilation and air conditioning
I <sup>2</sup> C	Inter Integrated Circuit
LCP	Liquid-crystal polymers
LSB	Least Significant Bit
MEMS	Micro-electro-mechanical system
MISO	Master In Slave Out
MOSI	Master Out Slave In
MSB	Most Significant Bit
PCB	Printed Circuit Board
SPI	Serial Peripheral Interface

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## Overview of helpful application notes and software resources

### Application notes

#### Application note ANM001 - MEMS Sensor PCB design and soldering guideline

<http://www.we-online.com/ANM001>

This technical document provides necessary information and general guidelines for soldering and PCB design for the Würth Elektronik eiSos MEMS sensor products with an LGA surface-mount package.

#### Application note ANR034 - How to use Zephyr sensor drivers

<http://www.we-online.com/ANR034>

The application note shows how to integrate the Zephyr drivers of Würth Elektronik eiSos sensors into the user's application source code to use Würth Elektronik eiSos sensors in the user's end device.

### Software resources

[https://github.com/WurthElektronik/Sensors-SDK\\_STM32](https://github.com/WurthElektronik/Sensors-SDK_STM32)

The Sensor Software Development Kit (SDK) is a set of software tools that enable rapid software integration of Würth Elektronik eiSos sensors into the application software on the host MCU.

### Zephyr sensor driver

<https://github.com/zephyrproject-rtos/zephyr/tree/main/drivers/sensor/wsen/>

The Zephyr sensor driver provides native support for integrating Würth Elektronik eiSos sensors into applications running on the Zephyr RTOS.

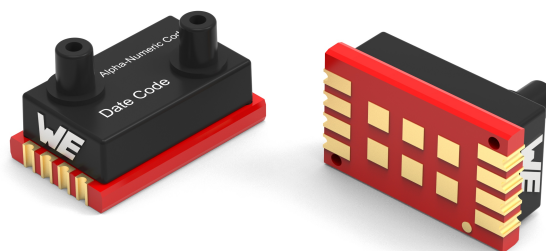
# 1 Introduction

The differential pressure sensors from Würth Elektronik eiSos allow measurement of pressure difference between two vertical pressure ports. The sensors consist of a MEMS based piezo-resistive sensing element and an ASIC integrated on a ceramic substrate. On-chip calibration, temperature compensation and signal conditioning provide highly accurate pressure in both digital and analog forms. Digital pressure data can be accessed by interfacing the sensor to the host controller via digital I<sup>2</sup>C interface. Simple communication protocol enables easy integration of the software, without the need of programming internal registers.

The sensors, available in various pressure ranges can measure differential pressure up to 10 bar. They are intended to be used for non-corrosive gases such as air and other dry gases (see section 2.2 for further information). The sensors come in 13 x 8 mm reflow solderable surface mount package with two pressure ports on top, allowing manifold mounting or two barbed ports on the side for direct tube connections.

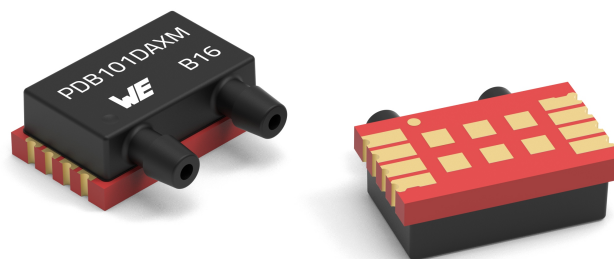
## 1.1 Applications

- HVAC
- Filter monitoring
- Gas leak detection
- Inhalers
- Fume hood



## 1.2 Key features

- Available in different pressure ranges
  - $\pm 1$  kPa
  - $\pm 10$  kPa
  - $\pm 350$  kPa
  - 0 to 100 kPa
  - -100 kPa to 1000 kPa
- Supply voltage: 3 V to 5.5 V
- Analog and digital output for pressure
- Communication interface: I<sup>2</sup>C, SPI
- Temperature range: -25 °C to 85 °C
- Typical current consumption: 4.6 mA



## 1.3 Ordering information

WE order code	Pressure range [kPa]	Marking	Dimensions [mm]	Description
2513130810105	$\pm 1$	PDB101DAXM	13.3x11.6 x4.5	Horizontal barbed ports
2513130810205	$\pm 10$	PDB102DAXM		
2513130835205	$\pm 35$	PDB352DAXM		
2513130810305	0 to 100	PDU103DAXM	13.3 x 8.0 x 7.5	Vertical straight ports
2513130810405	-100 to 1000	PDU104DAXM		

Table 1: Ordering information

## 1.4 Block diagram

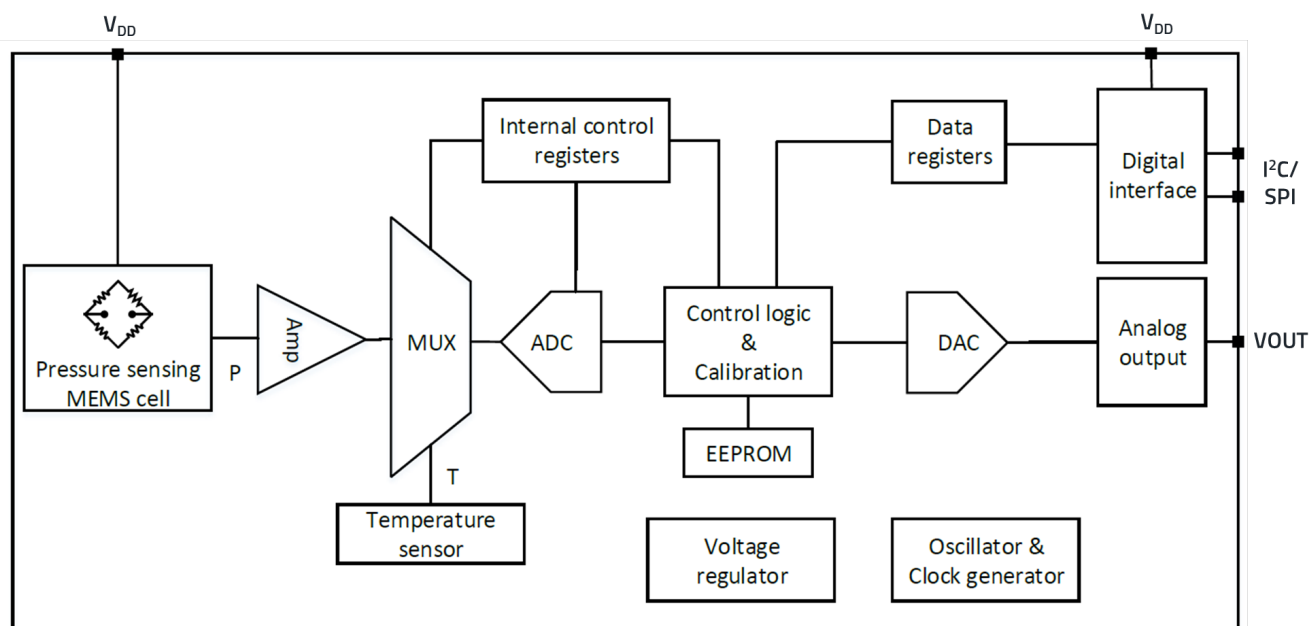


Figure 1: Block diagram

MEMS based piezo-resistors embedded on a suspended silicon membrane is the primary sensing element of the device. The piezo-resistors, connected in a Wheatstone bridge configuration produce analog output voltage proportional to the applied pressure.

Analog output is converted to the digital values through delta-sigma analog-to-digital converter (ADC). The ASIC embeds a high-resolution temperature sensor which is used for internal compensation of the pressure signal.

Each sensor is calibrated at three temperature and three pressure points. The trimming parameters and calibration coefficients (stored in the on-chip EEPROM) are used for the digital signal correction. The digital pressure values are available for the user to read via digital I<sup>2</sup>C or SPI interface.

Additionally, the sensors also provide optional digital temperature values for the temperature measurements via embedded temperature sensor.

Pressure values are also available as an analog voltage output. 16-bit digital to analog converter (DAC) embedded in the ASIC provides a calibrated analog voltage output through the *VOUT* pin.

## 2 Sensor specifications

### 2.1 General information

Parameter	Value
Operating temperature	-25 °C up to +85 °C
Compensated temperature range <sup>1</sup>	0 °C to 70 °C
Storage conditions (in original packaging)	up to 125 °C; < 75% RH
Communication interface	I <sup>2</sup> C, SPI and Analog
Moisture sensitivity level (MSL)	1
Electrostatic discharge protection (HBM)	2 kV

Table 2: General information

### 2.2 Media compatibility

Port	Information
High pressure port <sup>2</sup>	Dry and non-corrosive gases compatible with silicon, RTV, ceramics Al <sub>2</sub> O <sub>3</sub> , Pyrex and LCP plastics
Low pressure port	Dry and non-corrosive gases compatible with silicon, RTV, ceramics Al <sub>2</sub> O <sub>3</sub> , Pyrex, epoxy and FR4

Table 3: Media compatibility

<sup>1</sup>The sensor output will be within the specified performance limits in this temperature range.

<sup>2</sup>Refer to figure 22 and figure 23 for port identification.

## 2.3 Absolute maximum ratings

Absolute maximum ratings are the limits, the device will withstand without permanent damage.

Parameter	Symbol	Part number	Value		Unit
			Min	Max	
Input voltage $V_{DD}$ pin	$V_{DD\_MAX}$	25131308xxx05	-0.3	6	V
Input voltage control pins	$V_{IN\_MAX}^3$	25131308xxx05	-0.3	$V_{DD} + 0.3$	V
Differential over pressure <sup>4</sup>	$P_{OVER}$	2513130810105		10	kPa
		2513130810205		100	
		2513130835205		100	
		2513130810305		300	
		2513130810405		2500	
Differential burst pressure <sup>5</sup>	$P_{BURST}$	2513130810105		10	kPa
		2513130810205		150	
		2513130835205		170	
		2513130810305		500	
		2513130810405		2500	

Table 4: Absolute maximum ratings



The device is susceptible to be damaged by electrostatic discharge (ESD). Always use proper ESD precautions when handling. Improper handling of the device can cause performance degradation or permanent damage.

<sup>3</sup>SCL, SDA/MISO, SAO/CS and MOSI are control pins. The maximum allowed voltage on these pins must be always < 6 V.

<sup>4</sup>This is the pressure that may be applied to the sensor without causing damage to the sensing element. However, exposure to higher pressure may cause permanent damage to the sensor.

<sup>5</sup>This is the pressure that may be applied to the sensor without causing leakage and permanent damage to the sensing element.

## 2.4 Pressure sensor specifications

Unless otherwise stated, all the specified values were measured under the following condition:  
 $V_{DD} = 3.3 \text{ V}$  ;  $T = 25 \text{ }^{\circ}\text{C}$

### 2.4.1 Common paramters

Following pressure sensor parameters are applicable to part number: 25131308xxx05

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Nonlinearity <sup>6</sup>	$ACC_{P\_NL}$		-0.3	$\pm 0.1$	0.3	%FSS
Resolution (ADC)	$RES_P$			16		bit
Resolution (DAC)	$RES_{P\_DAC}$			16		bit
Response time	$t_{RESP}$			2.5		ms

Table 5: Pressure sensor specifications (part nr.: 25131308xxx05)



Full Scale Span (FSS) is the algebraic difference between the sensor output at the maximum and minimum pressure of the measurement range ( $P_{RANGE}$ ).

<sup>6</sup>Nonlinearity is the maximum deviation of the sensor output from the straight line fit (BFSL) across the entire pressure measurement.

## 2.4.2 Part number specific parameters

Unless otherwise stated, all the specified values were measured under the following condition:  
 $V_{DD} = 3.3 \text{ V}$  ;  $T = 25 \text{ }^{\circ}\text{C}$

### Part number: 2513130810105

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	$P_{\text{RANGE}}$		-1		1	kPa
Absolute accuracy <sup>7</sup>	$ACC_{P\_ABS}$	$T = 25 \text{ }^{\circ}\text{C}$	-1	$\pm 0.5$	1	%FSS
Total accuracy <sup>8</sup>	$ACC_{P\_TOT}$	$T = 0 \text{ to } 70 \text{ }^{\circ}\text{C}$	-1.5	$\pm 0.75$	1.5	%FSS
Sensitivity (digital)	$SEN_P$			$7.63 \times 10^{-5}$		kPa/digit
Repeatability <sup>9</sup>	$ACC_{P\_REP}$			$\pm 0.05$		%FSS
Long term drift	$ACC_{P\_DRIFT}$			$\pm 0.1$		%FSS

Table 6: Pressure sensor specifications (part nr.: 2513130810105)

### Part number: 2513130810205

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	$P_{\text{RANGE}}$		-10		10	kPa
Absolute accuracy <sup>7</sup>	$ACC_{P\_ABS}$	$T = 25 \text{ }^{\circ}\text{C}$	-1	$\pm 0.5$	1	%FSS
Total accuracy <sup>8</sup>	$ACC_{P\_TOT}$	$T = 0 \text{ to } 70 \text{ }^{\circ}\text{C}$	-1.5	$\pm 0.75$	1.5	%FSS
Sensitivity (digital)	$SEN_P$			$7.63 \times 10^{-4}$		kPa/digit
Repeatability <sup>9</sup>	$ACC_{P\_REP}$			$\pm 0.05$		%FSS
Long term drift	$ACC_{P\_DRIFT}$			$\pm 0.1$		%FSS

Table 7: Pressure sensor specifications (part nr.: 2513130810205)

<sup>7</sup> Absolute accuracy includes effects of non-linearity, pressure hysteresis, offset, span and repeatability at room temperature.

<sup>8</sup> Total accuracy includes all effects of offset, non-linearity, pressure hysteresis, span, repeatability and thermal effects between the compensated temperature range  $0 \text{ }^{\circ}\text{C}$  and  $70 \text{ }^{\circ}\text{C}$ .

<sup>9</sup> Repeatability is the typical deviation of the sensor output after 10 pressure cycles.

**Part number: 2513130835205**

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	P <sub>RANGE</sub>		-35		35	kPa
Absolute accuracy <sup>7</sup>	ACC <sub>P_ABS</sub>	T = 25 °C	-1	±0.5	1	%FSS
Total accuracy <sup>8</sup>	ACC <sub>P_TOT</sub>	T = 0 to 70 °C	-0.75	±0.25	0.75	%FSS
Sensitivity (digital)	SEN <sub>P</sub>			2.67 × 10 <sup>-3</sup>		kPa/digit
Repeatability <sup>9</sup>	ACC <sub>P_REP</sub>			±0.05		%FSS
Long term drift	ACC <sub>P_DRIFT</sub>			±0.1		%FSS

Table 8: Pressure sensor specifications (part nr.: 2513130835205)

**Part number: 2513130810305**

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	P <sub>RANGE</sub>		0		100	kPa
Absolute accuracy <sup>7</sup>	ACC <sub>P_ABS</sub>	T = 25 °C	-0.3	±0.1	0.3	%FSS
Total accuracy <sup>8</sup>	ACC <sub>P_TOT</sub>	T = 0 to 70 °C	-0.75	±0.25	0.75	%FSS
Sensitivity (digital)	SEN <sub>P</sub>			3.815 × 10 <sup>-3</sup>		kPa/digit
Repeatability <sup>9</sup>	ACC <sub>P_REP</sub>			±0.01		%FSS
Long term drift	ACC <sub>P_DRIFT</sub>			±0.05		%FSS

Table 9: Pressure sensor specifications (part nr.: 2513130810305)

<sup>7</sup> Absolute accuracy includes effects of non-linearity, pressure hysteresis, offset, span and repeatability at room temperature.

<sup>8</sup> Total accuracy includes all effects of offset, non-linearity, pressure hysteresis, span, repeatability and thermal effects between the compensated temperature range 0 °C and 70 °C.

<sup>9</sup> Repeatability is the typical deviation of the sensor output after 10 pressure cycles.

**Part number: 2513130810405**

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	P <sub>RANGE</sub>		-100		1000	kPa
Absolute accuracy <sup>7</sup>	ACC <sub>P_ABS</sub>	T = 25 °C	-0.3	±0.1	0.3	%FSS
Total accuracy <sup>8</sup>	ACC <sub>P_TOT</sub>	T = 0 to 70 °C	-0.75	±0.25	0.75	%FSS
Sensitivity (digital)	SEN <sub>P</sub>			4.196 × 10 <sup>-2</sup>		kPa/digit
Repeatability <sup>9</sup>	ACC <sub>P_REP</sub>			±0.01		%FSS
Long term drift	ACC <sub>P_DRIFT</sub>			±0.05		%FSS

Table 10: Pressure sensor specifications (Part nr.: 2513130810405)

<sup>7</sup> Absolute accuracy includes effects of non-linearity, pressure hysteresis, offset, span and repeatability at room temperature.

<sup>8</sup> Total accuracy includes all effects of offset, non-linearity, pressure hysteresis, span, repeatability and thermal effects between the compensated temperature range 0 °C and 70 °C.

<sup>9</sup> Repeatability is the typical deviation of the sensor output after 10 pressure cycles.

## 2.5 Temperature sensor specifications

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Measurement range	$T_{\text{RANGE}}$		0		70	°C
Resolution	$T_{\text{RES}}$			15		bits
Sensitivity	$\text{SEN}_T$			$4.272 \times 10^{-3}$		°C/digit

Table 11: Temperature sensor specifications

## 2.6 Electrical specifications

Unless otherwise stated, all the specified values were measured under the following conditions:  
 $T = 25\text{ °C}$ .

Parameter	Symbol	Test conditions	Value			Unit
			Min	Typ	Max	
Operating supply voltage	$V_{\text{DD}}$		3.0		5.5	V
Current consumption	$I_{\text{DD}}$	$V_{\text{DD}} = 5.0\text{ V}$		4.6		mA
Output current analog pin	$I_{\text{OUT\_A}}$				1	mA
Digital input voltage-high-level	$V_{\text{IH}}$		$0.7 * V_{\text{DD}}$			V
Digital input voltage-low-level	$V_{\text{IL}}$				$0.3 * V_{\text{DD}}$	V
Digital output voltage-high-level	$V_{\text{IH}}$		$0.85 * V_{\text{DD}}$			V
Digital output voltage-low-level	$V_{\text{IL}}$				$0.1 * V_{\text{DD}}$	V

Table 12: Electrical specifications



For a stable current consumption when only "Analog output" is used it is recommended to connect SCL and SDA pins to ground.

## 3 Pinning information

### 3.1 Pin configuration

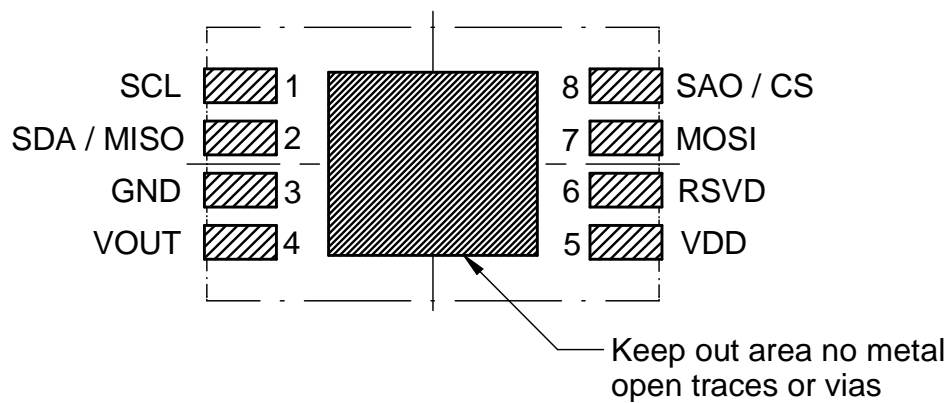


Figure 2: Pin specifications - vertical nozzles (top view)

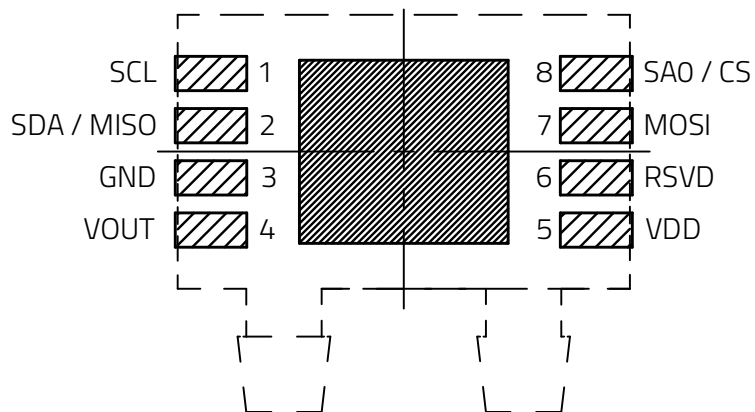


Figure 3: Pin specifications- horizontal nozzles (top view)

## 3.2 Pin description

Pin No.	Name	Function	Input/output	Comments
1	<i>SCL</i>	I <sup>2</sup> C /SPI serial clock	Input	
2	<i>SDA / MISO</i>	I <sup>2</sup> C serial data /SPI data out	Input/Output	
3	<i>GND</i>	Negative supply voltage	Supply	
4	<i>VOOUT</i>	Analog output	Output	
5	<i>VDD</i>	Positive supply voltage	Supply	
6	<i>RSVD</i>	Reserved		Do not connect
7	<i>MOSI</i>	SPI data in	Input	
8	<i>SA0 / CS</i>	I <sup>2</sup> C address select / SPI chip select	Input	

Table 13: Pin description

## 4 Digital interface: I<sup>2</sup>C

The sensor supports standard I<sup>2</sup>C (Inter Integrated Circuit) bus protocol. I<sup>2</sup>C is a serial 8-bit protocol with two-wire interface that supports communication between different ICs, for example, between microcontrollers and other peripheral devices<sup>1</sup>.

### 4.1 General characteristics

A serial data line (*SDA*) and a serial clock line (*SCL*) are required for the communication between the devices connected via I<sup>2</sup>C bus. Both *SDA* and *SCL* lines are bidirectional. The output stages of devices connected to the bus must have an open-drain or open-collector. Therefore, the *SDA* and *SCL* lines are connected to a positive supply voltage via pull-up resistors. In I<sup>2</sup>C protocol, the communication is realized through master-slave principle. A master device generates the clock pulse, a start condition and a stop condition for the data transfer. Each connected device on the bus is addressable via a unique address. Master and slave can act as a transmitter (master-transmitter or slave transmitter) or a receiver (master receiver or slave receiver) depending upon whether the data needs to be sent or received.



This sensor functions like a slave device on the I<sup>2</sup>C bus.

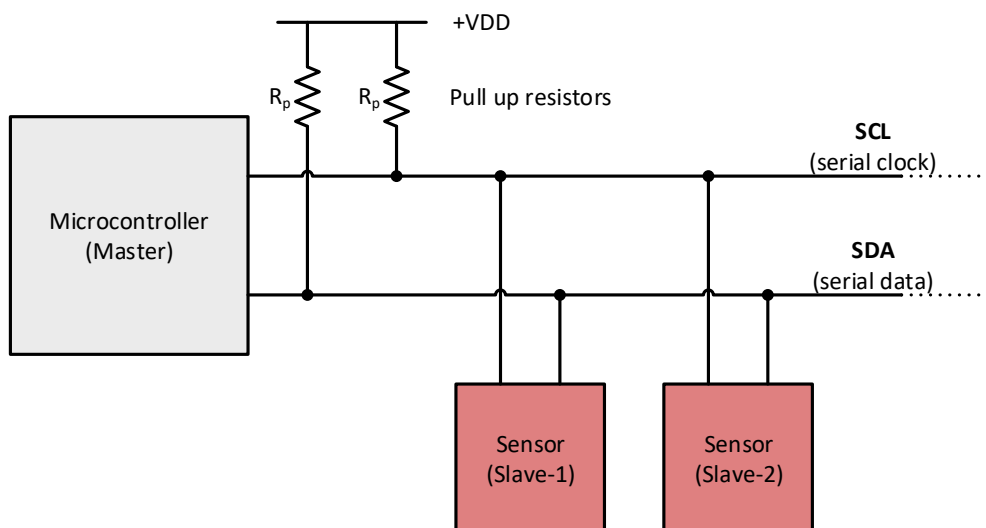


Figure 4: Master-slave concept

<sup>1</sup>Further information about the I<sup>2</sup>C interface can be found at <https://www.nxp.com/docs/en/user-guide/UM10204.pdf>.

## 4.2 SDA and SCL logic levels

The positive supply voltage to which *SDA* and *SCL* lines are pulled up (through pull-up resistors), in turn determines the high level input for the slave devices. Input reference levels for this sensor are set as  $0.7 \times V_{DD}$  (for logic high) and  $0.3 \times V_{DD}$  (for logic low). See figure 5.

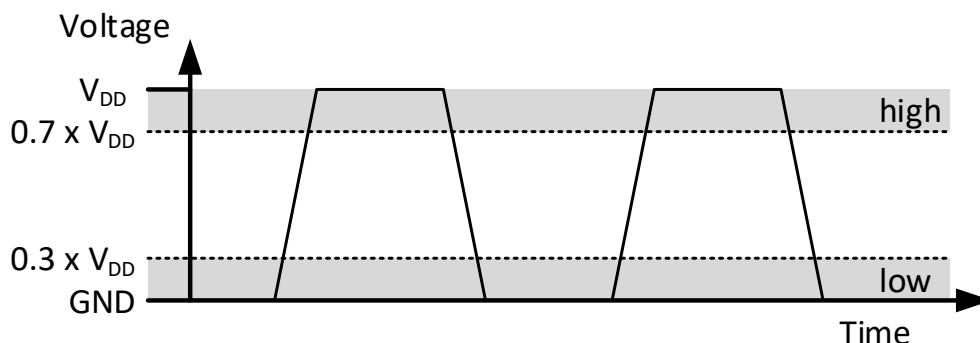


Figure 5: *SDA* and *SCL* logic levels

## 4.3 Communication phase

### 4.3.1 Idle state

During the idle state, the bus is free and both *SDA* and *SCL* lines are in logic high '1' state.

### 4.3.2 START (S) and STOP (P) condition

Data transfer on the bus starts with a START command, which is generated by the master. A start condition is defined as a high-to-low transition on the *SDA* line while the *SCL* line is held high. The bus is considered busy after the start condition.

Data transfer on the bus is terminated with a STOP command, which is also generated by the master. A low-to-high transition on the *SDA* line, while the *SCL* line being high is defined as a STOP condition. After the stop condition, the bus is considered free again and is in idle state.

Figure 6 shows the I<sup>2</sup>C bus START and STOP conditions.

Master can also send a REPEATED START (SR) command instead of STOP command. REPEATED START condition is the same as the START condition.

### 4.3.3 Data validity

After the start condition, one data bit is transferred with each clock pulse. The transmitted data is only valid when the *SDA* line data is stable (high or low) during the high period of the clock pulse. High or low state of the data line can only change when clock pulse is in low state.

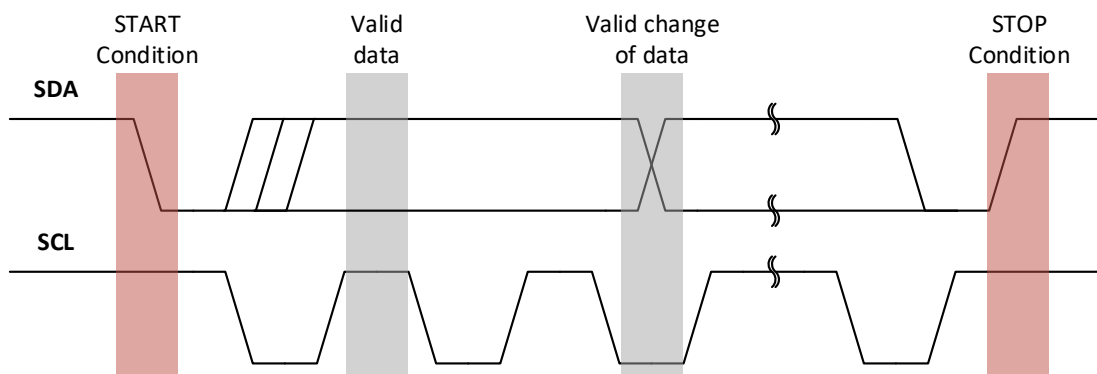


Figure 6: Data validity, START and STOP condition

### 4.3.4 Byte format

Data transmission on the *SDA* line is always done in bytes, with each byte being 8-bits long. Data is transferred with the most significant bit (MSB) followed by other bits.

If the slave cannot receive or transmit another complete byte of data, it can force the master into a wait state by holding *SCL* low. Data transfer continues when the slave is ready which is indicated by releasing the *SCL* line.

### 4.3.5 Acknowledge (ACK) and No-Acknowledge (NACK)

Each byte sent on the data line must be followed by an Acknowledge bit. The receiver (master or slave) generates an Acknowledge signal to indicate that the data byte was received successfully and another data byte could be sent.

After one byte is transmitted, the master generates an additional Acknowledge clock pulse to continue the data transfer. The transmitter releases the *SDA* line during this clock pulse so that the receiver can pull the *SDA* line to low state in such a way that the *SDA* line remains stable low during the entire high period of the clock pulse. This is considered as an Acknowledge signal.

In case the receiver does not want to receive any further byte, it does not pull down the *SDA* line and it remains in stable high state during the entire clock pulse. This is considered as a No-Acknowledge signal and the master can generate either a stop condition to terminate the data transfer or a repeated start condition to initiate a new data transfer.

#### 4.3.6 Slave address for the sensor

The slave address is transmitted after the start condition. Each device on the I<sup>2</sup>C bus has a unique address. Master selects the slave by sending corresponding address after the start condition. A slave address is 7 bits long followed by a Read/Write bit.

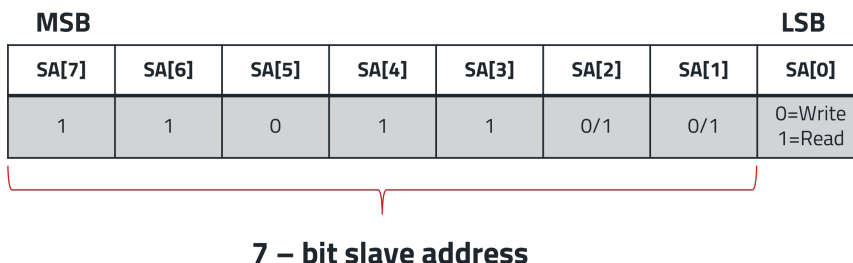


Figure 7: Slave address format

In I<sup>2</sup>C mode, slave address bit SA[2] is defined by the pin SA0/CS. SA[1] bit selects - either CRC protected or unprotected the I<sup>2</sup>C communication mode. Thus, each device can be configured with two separate I<sup>2</sup>C addresses. The CRC protected and unprotected communication can be interleaved.

7-bit Slave address (Hex)	
<b>0x6C</b>	SA0/CS pin connected to GND via Pull-down ; data read with CRC
0x6D	SA0/CS pin connected to GND via Pull-down ; data read without CRC
<b>0x6E</b>	SA0/CS pin connected to VDD via Pull-up; data read with CRC
0x6F	SA0/CS pin connected to VDD via Pull-up; data read without CRC

Figure 8: Slave address configuration

The R/W bit determines the transmission direction of the following bytes. A '0' indicates a write operation (transmission from master to slave) and a '1' indicates a read operation (request data from slave).

### 4.3.7 Read/Write operation

Once the slave-address and data direction bit is sent, the slave acknowledges the master. The slave can then transmit multiple number of data bytes. Each transmitted data byte is followed by an Acknowledgement from the master. If the master no longer wants to receive further data from the slave, it would send No-Acknowledge (NACK). Afterwards, Master can send a STOP condition to terminate the data transfer.

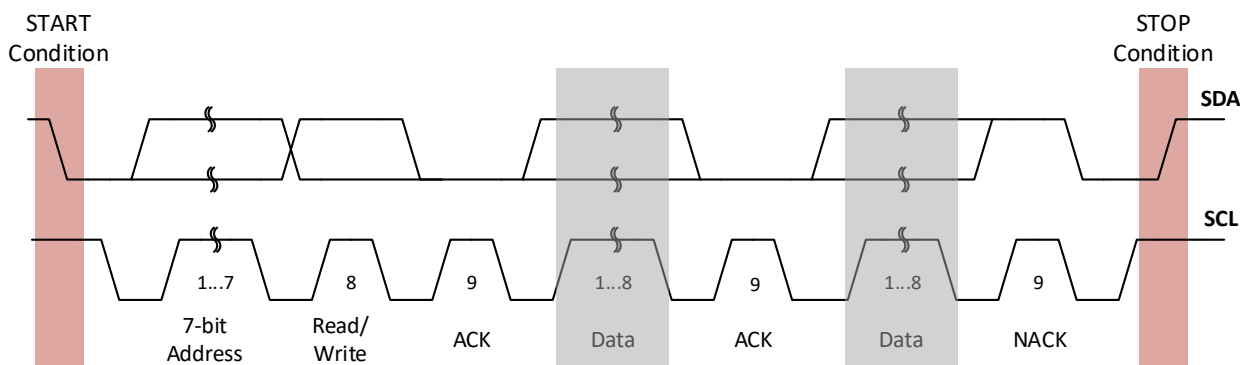


Figure 9: Complete data transfer

The host controller (master) can read from and write to the memory addresses (registers) of the slave using following commands.

#### Random write

Sets a starting register address and writes data sequentially to the device beginning from that register.

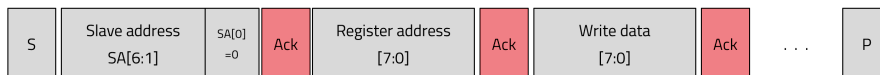
#### Random read

Sets a starting register address and reads sequential data from the device starting at that register.

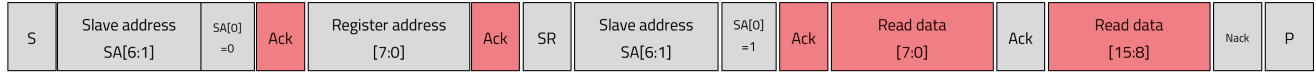
#### Read last

Reads data from the device beginning at the last register address set by the master, allowing repeated reads without resending the register address.

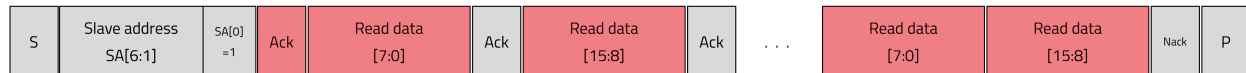
### Random write



### Random read



### Repeated read



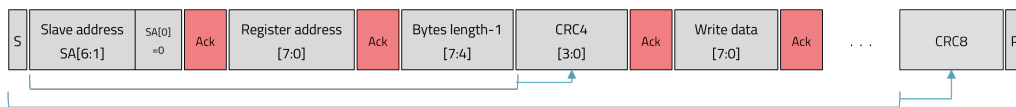
S = Start condition  
P = Stop condition  
ACK = Acknowledge  
Nack = No acknowledge  
SR = Repeated start condition

Transmission from master to slave

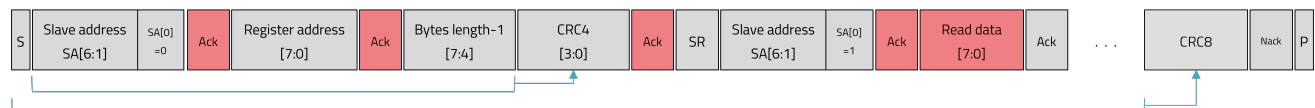
Transmission from slave to master

Figure 10: I<sup>2</sup>C Read and Write command format (No CRC)

### Random write (with CRC)



### Random read (with CRC)



S = Start condition  
P = Stop condition  
ACK = Acknowledge  
Nack = No acknowledge  
SR = Repeated start condition

Transmission from master to slave

Transmission from slave to master

Figure 11: I<sup>2</sup>C Read and Write command format (with CRC)

The R/W bit SA[0] follows the 7-bit slave address and defines the direction of data transfer. A R/W bit = '0' indicates a write operation (data is written to slave), and R/W = 1 indicates a read operation (data is read from slave).

This is followed by the register address which specifies the location of the register to be written to or read from.

The read/write data is transferred MSB first - low byte before the high byte.

In case of the CRC read/write operation it is also necessary to indicate the number of bytes to be transferred. The length field (bits [7:4]) indicates the number of data bytes to transfer,

minus one. For example, a value of '0001' means two bytes will be transferred. All frames must transfer an even number of bytes.



The maximum length for CRC-protected read/write frames is 4 bytes each. For unprotected frames, the length is unlimited.

CRC4 and CRC8 are computed in the same bit and byte order as they are transmitted over the bus. The CRC polynomials are defined as below to compute the CRC.

	Polynomial	Initialization value
CRC4	$0x03 (x^4 + x^1 + x^0)$	0x0F
CRC8	$0xD5 (x^8 + x^7 + x^6 + x^4 + x^2 + x^0)$	0xFF

Table 14: CRC Polynomial



To detect communication errors, it is generally recommended that the master checks the ACK/NACK bits from the slave. If CRC is used, the master should also verify the CRC8 checksums sent by the slave.



if a random read/write command is aborted by a CRC4 error, no read last command may follow.

## 4.4 I<sup>2</sup>C timing parameters

Parameter	Symbol	Min	Max	Unit
<i>SCL</i> clock frequency	$f_{SCL}$	100	400	kHz
LOW period for <i>SCL</i> clock	$t_{LOW\_SCL}$	1.3		$\mu s$
HIGH period for <i>SCL</i> clock	$t_{HIGH\_SCL}$	0.8		$\mu s$
Hold time for START condition	$t_{HD\_S}$	0.6		$\mu s$
Setup time for (repeated) START condition	$f_{SCL}$	600		ns
<i>SDA</i> setup time	$t_{SU\_SDA}$	100		ns
<i>SDA</i> data hold time	$t_{HD\_SDA}$	0		ns
Setup time for STOP condition	$t_{SU\_P}$	600		ns
Bus free time between STOP and START condition	$t_{BUF}$	600		ns

Table 15: I<sup>2</sup>C timing parameters

## 5 Digital interface: Serial Peripheral Interface (SPI)

Serial Peripheral Interface (SPI) is a synchronous serial communication bus system for the communication between host microcontroller and other peripheral ICs such as ADCs, EEPROMs, sensors, etc. SPI is a full-duplex master-slave based interface allowing the communication to happen in both directions simultaneously. The data from the master or the slave is synchronized either on the rising or falling edge of clock pulse. 4-wire interface consists of two signal lines and two data lines.

1. Clock (SCL)
2. Chip select (CS)
3. Master out, slave in (MOSI)
4. Master in, slave out (MISO)

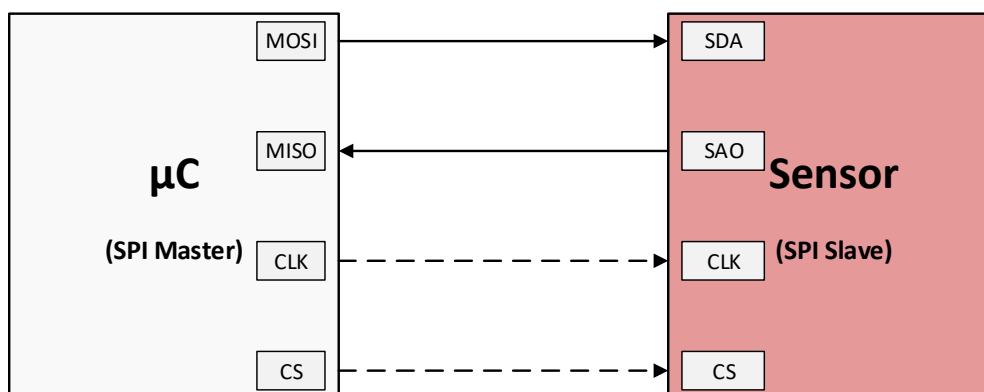


Figure 12: SPI Interface

Master generates the clock signal and is connected to all slave devices. Data transmission between the master and slaves is synchronized to the clock signal generated by the master.

One master can be connected to one or more slave devices. Each slave device is addressed and controlled by the master via individual chip select (CS) signals. CS is controlled by the master and is normally an active low signal.

MOSI and MISO are data lines. MOSI transmits data from the master to the slave. MISO transmits data from the slave to the master.



This sensor supports 4-Wire SPI interface

## 5.1 Communication modes

In SPI, the master can select the clock polarity (CPOL) and clock phase (CPHA). The CPOL bit sets the polarity of the clock signal during the idle state. The CPHA bit selects the clock phase. Depending on the CPHA bit, the rising or falling clock edge is used to sample and shift the data. Depending on the CPOL and CPHA bit selection in the SPI control registers, four SPI modes are available as per table 16. In order to ensure proper communication, master and the slave must be set to same communication modes.

CPOL	CPHA	Description
<b>0</b>	<b>0</b>	<b>Clock polarity LOW in idle state; Data sampled on the rising clock edge</b>
0	1	Clock polarity LOW in idle state; Data sampled on the falling clock edge
1	1	Clock polarity HIGH in idle state; Data sampled on the falling clock edge
1	0	Clock polarity HIGH in idle state; Data sampled on the rising clock edge

Table 16: SPI communication modes



The sensor operates by default in Mode 0, meaning CPOL and CPHA bits are '0'

## 5.2 Data transfer

Communication begins when the master selects a slave device by pulling the CS line to LOW. The clock and data lines (MOSI/MISO) are available for the selected slave device. Data stored in the specific shift registers are exchanged synchronously between master and the slave through MISO and MOSI lines. The data transmission is over when the CS line is pulled up to the HIGH state. 4-wire SPI uses both data lines for the synchronous data exchange in both the direction.

## 5.3 Sensor SPI Communication

4-Wire SPI of this sensor uses following lines: MOSI, MISO, SCL (serial clock) and CS (chip select). For more information, please refer to pin description in the section 3.2

The sensor supports following read and write commands:

### Random write:

Sets a starting register address and writes data sequentially to the device beginning from that register.

### Random read:

Sets a starting register address and reads sequential data from the device starting at that register.

SPI data transfer begins when CS is pulled LOW by the master. The communication ends when the CS is pulled high.

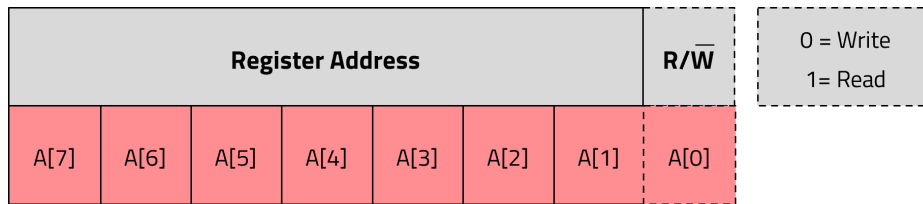


Figure 13: SPI register address

The register address field specifies the byte address of the first register to be read from or written to. Only word-aligned (16-bit) read/write operations are supported and the least significant bit (LSB) of the register address is not considered in the frame. Instead, the LSB of the first byte (register address) sent is used to indicate whether the operation is a read or a write. When R/W is '0', the data is written on to the sensor. When '1', the data is read from the sensor.



All read and write operations begin at word-aligned addresses and one word represents 16 bits (2 bytes), meaning the least significant bit (LSB) of the register address must be 0 and only even number of bytes transfer is allowed.

The next byte of data, specifies whether the SPI read/write operation is carried out with CRC or without CRC. Communication is CRC protected if the most significant bit (MSB) B[7] of the second byte sent by the master is '1'. Next three bits B[6:4] specifies the number of words (16 bit data) to be transferred minus one. For example, a value of '001' means two 16 bit words (4 bytes) will be transferred. If CRC protected transfer is used, bits B[3:0] are used to send the CRC4 value other wise these bits are always '0000'.

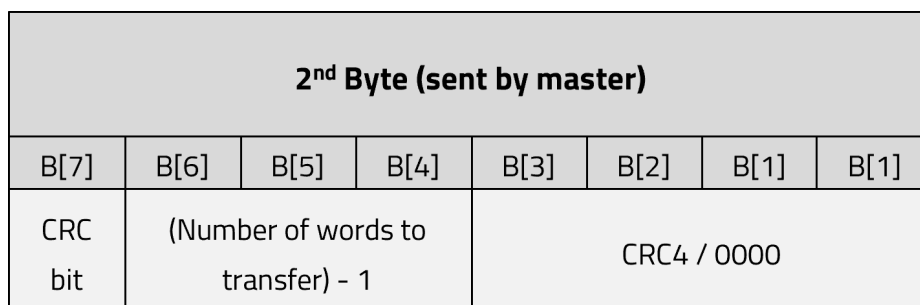


Figure 14: SPI CRC byte

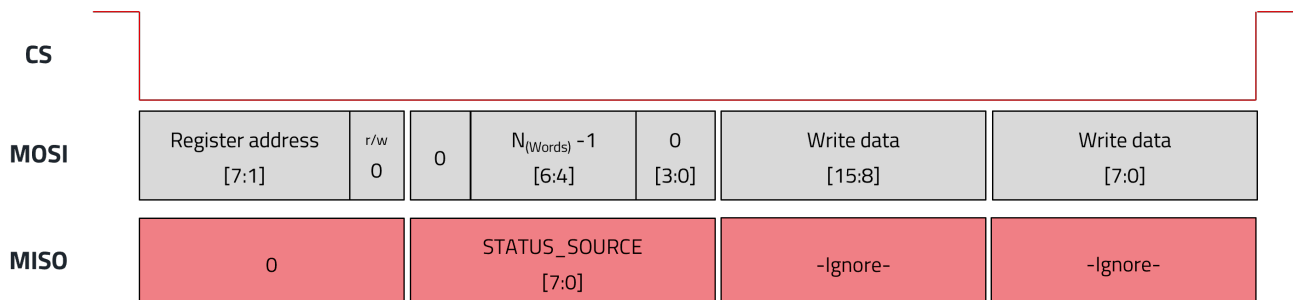
CRC4 and CRC8 are computed in the same bit and byte order as they are transmitted over the bus. The CRC polynomials are calculated as below

	Polynomial	Initialization value
CRC4	$0x03 (x^4 + x^1 + x^0)$	0x0F
CRC8	$0x07 (x^8 + x^7 + x^6 + x^4 + x^2 + x^0)$	0xF3

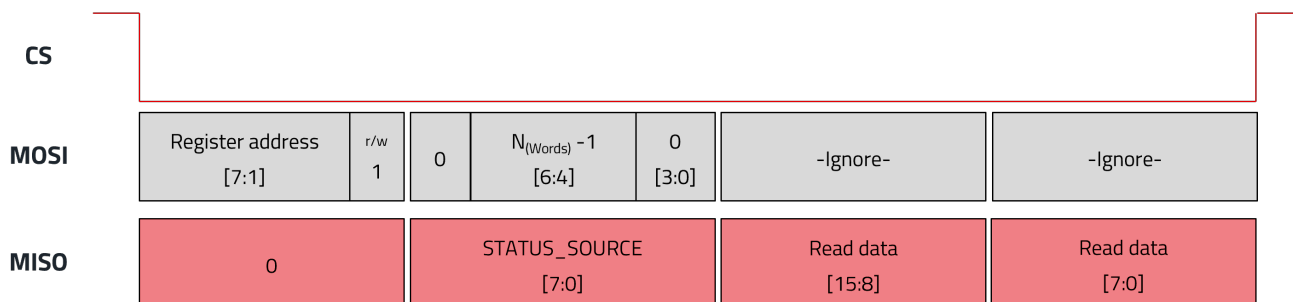
Table 17: CRC Polynomial

Complete SPI read and write sequence is showed in the figure 15 and figure 16

### Random write



### Random read



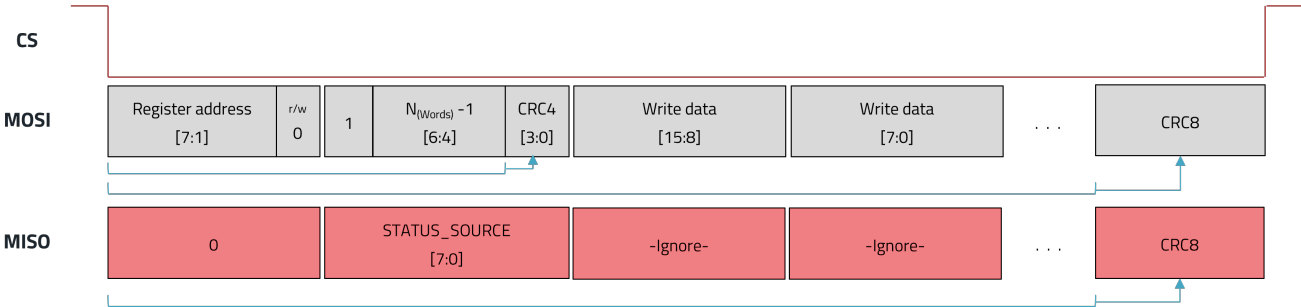
Transmission from master to slave      Transmission from slave to master

Figure 15: SPI Read and Write command format (No CRC)



For CRC-protected transfer, the maximum data length is 16 bytes for read operations and 4 bytes for write operations. For normal data transfer, the maximum read/write data length is 16 bytes.

Random write (with CRC)



Random read (with CRC)

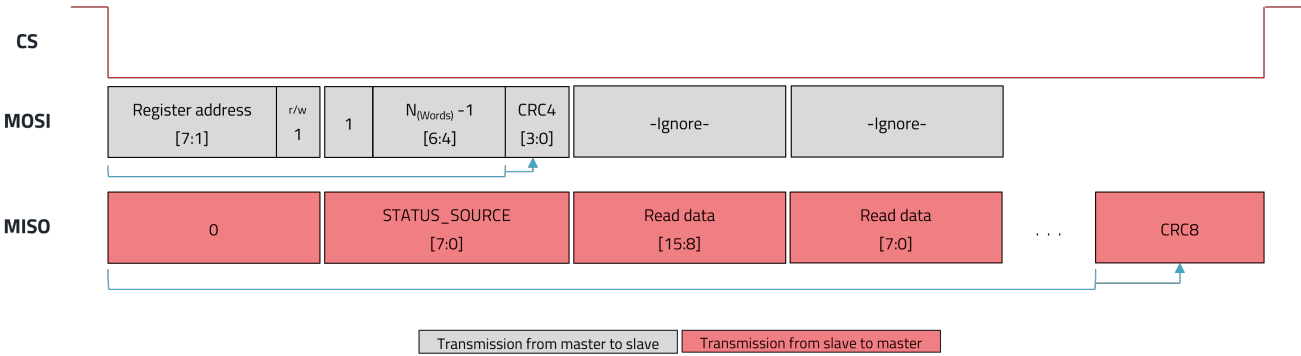


Figure 16: SPI Read and Write command format (with CRC)

## 6 Application circuit

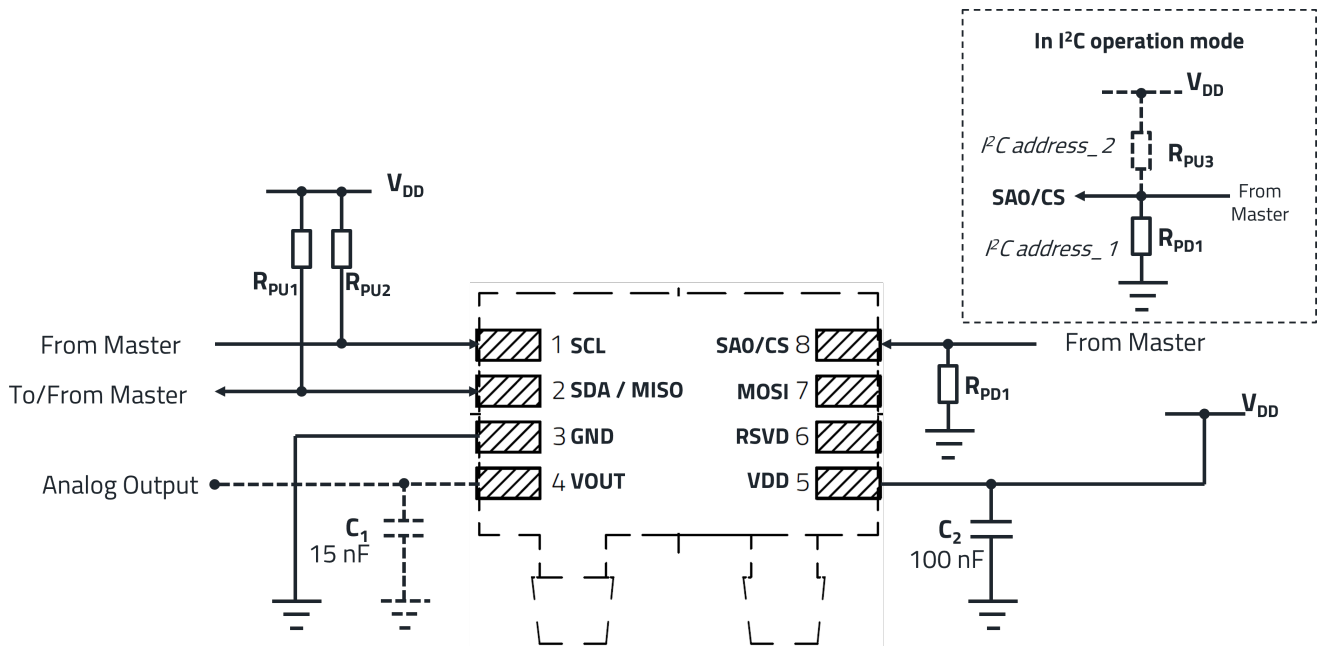


Figure 17: Application circuit with I<sup>2</sup>C interface (top view)

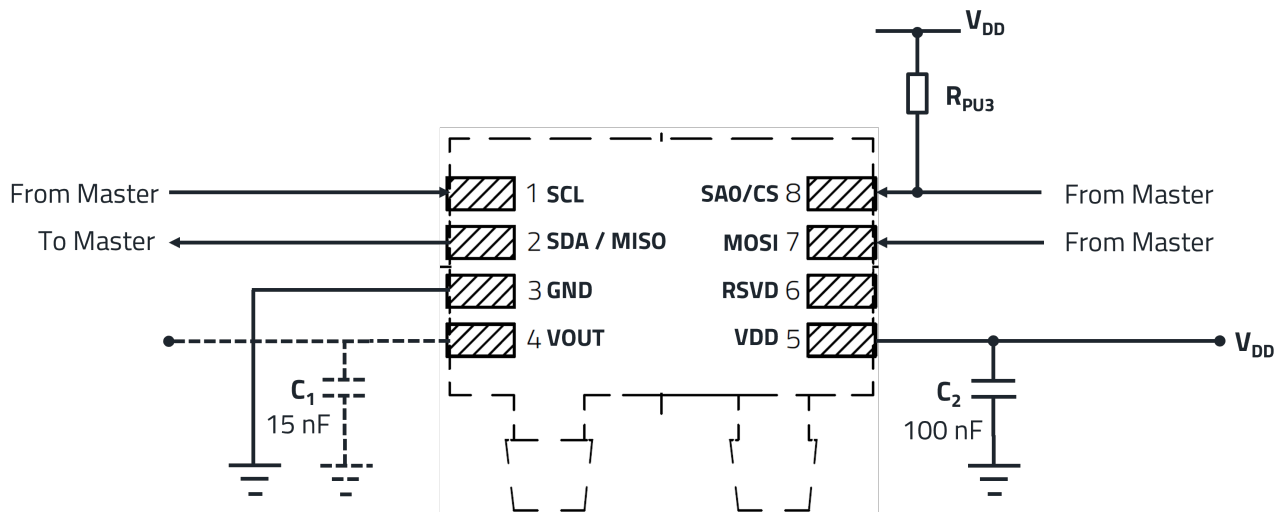


Figure 18: Application circuit with SPI interface (top view)

$V_{DD}$  pin is the central supply pin for the MEMS cell and internal circuits. In order to prevent ripple from the power supply, a decoupling capacitor of 100 nF must be placed as close to the  $V_{DD}$  pad of the sensor as possible. Further, a decoupling capacitor of 15 nF should be placed between  $V_{OUT}$  and ground.

Pin  $SA0/CS$  has dual functionality based on how it is connected.

#### **$SA0/CS$ at power on:**

- Connected to  $GND$  via external pull-down resistors enables I<sup>2</sup>C communication
- Connected to  $V_{DD}$  via external pull-up resistors enables SPI communication

#### **$SA0/CS$ in operation:**

- In I<sup>2</sup>C communication mode  $SA0/CS$  defines the I<sup>2</sup>C slave address.  
 $SA0/CS$  connected to  $GND$  defines the I<sup>2</sup>C slave address 0x6C  
 $SA0/CS$  connected to  $V_{DD}$  defines the I<sup>2</sup>C slave address 0x6E
- In SPI mode, the master (host microcontroller) pulls down the  $SA0/CS$  pin indicating start of data transfer.

When using I<sup>2</sup>C interface,  $SCL$  and  $SDA$  must be connected to  $V_{DD}$  through the pull-up resistors  $R_{PU1}$  and  $R_{PU2}$ . Proper value of the pull-up resistors must be chosen depending on the I<sup>2</sup>C bus speed and load. The sensor does not have internal pull-up resistors.



Pull-up resistors  $R_{PU1}$  and  $R_{PU2}$  on  $SCL$  and  $SDA$  must be  $>2.2\text{ k}\Omega$

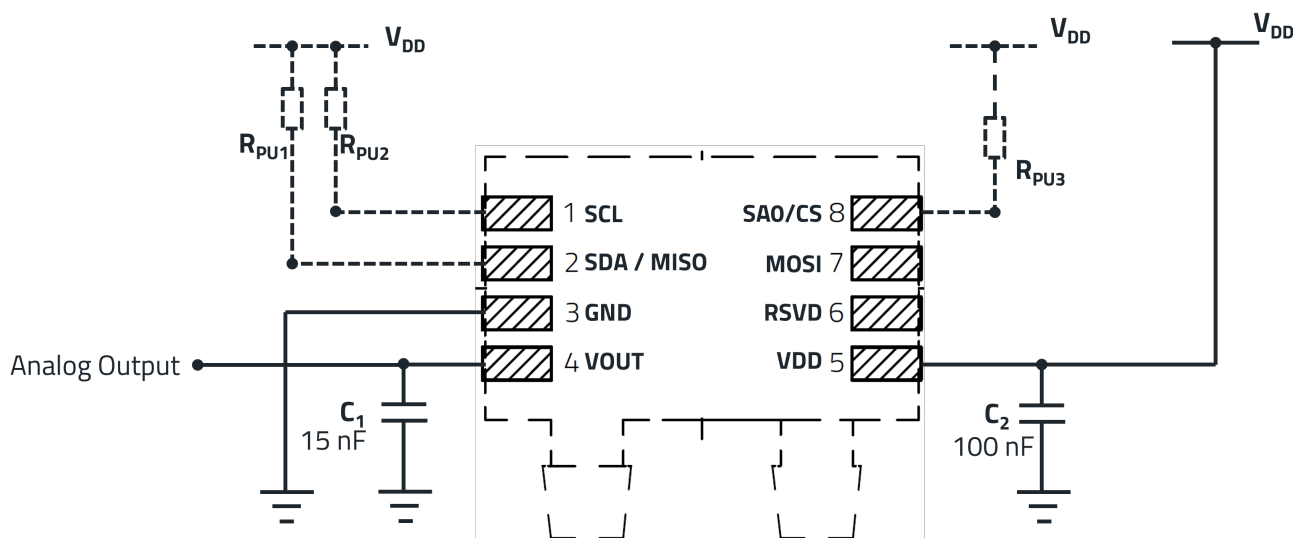


Figure 19: Application circuit with analog interface (top view)

For a stable operation of sensor in analog only mode, it is recommended to connect  $SCL$ ,  $SDA/MISO$  and  $SA0/CS$  pins either to  $V_{DD}$  via pull-up resistors or to ground via pull-down resistors.



If both digital I<sup>2</sup>C and analog interfaces are used simultaneously, it is recommended to route these lines as far from each other as possible.

Avoid routing of the *VDD*, *SDA/MISO*, *MOSI* and *SA0 / CS* lines underneath the sensor (see section 11.3 for further information).

## 7 Register description

Register read and write operations are performed via the digital communication interface (I<sup>2</sup>C or SPI). After power-up, all registers are write-protected by default, except for the STATUS\_SOURCE and CMD registers.



All registers represent word-aligned (16-bit) data.

### 7.1 Command register

#### CMD (0x22)

Address: 0x22

Type (r/w): R/W

Default Value [15:0]: 0x00

This register controls the state of the sensor and can be used to put the sensor either to SLEEP mode or RESET mode.

Write bytes	Description
0x6C32	SLEEP mode Puts sensor in sleep mode. Internal IC is powered down and all I/O pins are in High-Impedance state. The sensor wakes up on the rising edge signal on SCL pin
0xB169	RESET Software reset is performed and power-up sequence is executed. Configuration parameters from the internal memory are copied to the registers and a CRC check is performed

Table 18: Command register

### 7.2 Temperature register

#### DSP\_T (0x2E)

Address: 0x2E

Type (r/w): R

Default Value [15:0]: 0x00

This register stores the calibrated temperature data in 16-bit signed format (two's complement)

## 7.3 Pressure register

### DSP\_P (0x30)

Address: 0x30

Type (r/w): R

Default Value [15:0]: 0x00

This register stores the calibrated pressure data in 16-bit signed format (two's complement)

## 7.4 Status register- synchronized

### STATUS\_SYNC (0x32)

Address: 0x32

Type (r/w): R/W

Default Value [15:0]: 0x00

Bit	Name	R/W	Type	Description
0	idle	R	status	Identical to bit idle in STATUS_SOURCE
1	reserved	-	-	-
2	reserved	-	-	-
3	dsp_p_up	R/W	event	Copy of STATUS_SOURCE register bit dsp_p_up when register DSP_P is read
4	dsp_t_up	R/W	event	Copy of STATUS_SOURCE register bit dsp_t_up when register DSP_T is read
5	reserved	-	-	-
6	reserved	-	-	-
7	reserved	-	-	-
8	reserved	-	-	-
9	reserved	-	-	-
10	reserved	-	-	-
11	crc_error	R	event	Identical to bit crc_error in STATUS_SOURCE
12	reserved	-	-	-
13	reserved	-	-	-
14	dsp_p_miss	R	event	Identical to bit dsp_p_miss in STATUS_SOURCE
15	dsp_t_miss	R	event	Identical to bit dsp_t_miss in STATUS_SOURCE

Table 19: STATUS\_SYNC register

STATUS\_SYNC (0x32) register bits [0, 11] and [15:14] are identical to the corresponding bits of register STATUS\_SOURCE (0x36) and are updated synchronously with STATUS\_SOURCE (0x36).



Bits [4:3] in the STATUS\_SYNC (0x32) register are copied from the respective bits of STATUS\_SOURCE (0x36) register only when the corresponding DSP\_x register is read.

Bits dsp\_t\_up and dsp\_p\_up are used to indicate whether the temperature (DSP\_T) and pressure (DSP\_P) values were freshly updated at the time of reading.

When a DSP\_T or DSP\_P register is read - the corresponding event bit (dsp\_t\_up or dsp\_p\_up) from the STATUS\_SOURCE register is first copied into the STATUS\_SYNC register. Immediately after this copy, the original bit in the STATUS\_SOURCE register is automatically cleared.

This ensures that the STATUS\_SYNC register accurately reflects the freshness of data at the moment it was read, enabling reliable synchronized polling of sensor values.



Performing a single read operation of three consecutive 16-bit words starting from address 0x2E (beginning with DSP\_T) ensures that the most recent temperature and pressure values are retrieved in a synchronized and efficient manner.



Event type bits remain set until cleared by writing '1' to the respective bit position in STATUS\_SOURCE (0x36).



Do not write to STATUS\_SYNC (0x32) register to clear the event flags.

## 7.5 Status register

### STATUS\_SOURCE (0x36)

Address: 0x36

Type (r/w): R/W

Default Value [15:0]: 0x00

Writing 0xFFFF to STATUS\_SOURCE register will clear all event bits

Bit	Name	R/W	Type	Description
0	idle	R/W	status	0: sensor in busy state; 1: Sensor in idle state
1	reserved	-	-	-
2	reserved	-	-	-
3	dsp_p_up	R/W	event	Set to '1' when DSP_P register is updated; This bit is either self cleared by reading DSP_P register or manually cleared by writing '1' to this bit
4	dsp_t_up	R/W	event	Set to '1' when DSP_T register is updated; This bit is either self cleared by reading DSP_T register or manually cleared by writing '1' to this bit
5	reserved	-	-	-
6	reserved	-	-	-
7	reserved	-	-	-
8	reserved	-	-	-
9	reserved	-	-	-
10	reserved	-	-	-
11	crc_error	R/W	event	Set to '1' when CRC error occurs during communication (this bit is not set if CRC4 error occurs during SPI mode)
12	reserved	-	-	-
13	reserved	-	-	-
14	dsp_p_miss	R/W	event	Set to '1' when bit dsp_p_up in STATUS_SOURCE was '1' and DSP_P register is updated again
15	dsp_t_miss	R/W	event	Set to '1' when bit dsp_t_up in STATUS_SOURCE was '1' and DSP_T register is updated again

Table 20: STATUS\_SOURCE register



The event-type bits [4:3] in the STATUS\_SOURCE (0x36) register are first copied into the corresponding bits of the STATUS\_SYNC (0x32) register when the associated DSP\_x register is read. After this, the bits in STATUS\_SOURCE are self cleared.

## 8 Reading digital output data

The sensor generates fully calibrated and temperature compensated digital pressure values which is available for the user to read through host controller. Sensor must be interfaced to the host controller via I<sup>2</sup>C or SPI interface.

### 8.1 Writing data via I<sup>2</sup>C

Figure 20 shows the sequence to put the sensor to SLEEP mode. 16-bits of data (0x6C32) needs to be written to the CMD (0x22) register. For the example, the 7-bit slave address is 0x6C.



In Sleep mode, the sensor is fully powered down and monitors only for a wake-up event. The device is awakened by the next rising edge on the SCL pin.

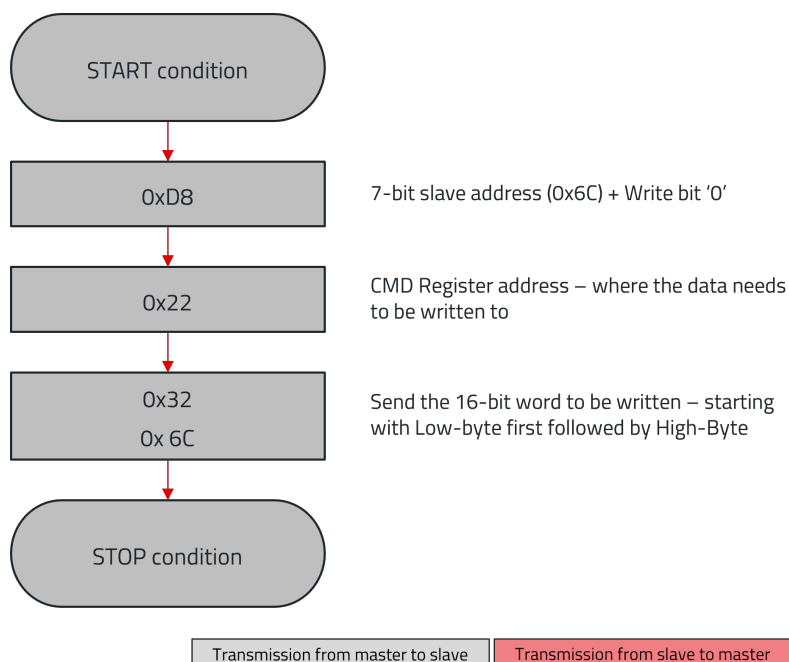


Figure 20: Write data with I<sup>2</sup>C interface

### 8.2 Reading data via I<sup>2</sup>C

Following example explains how to read the pressure and temperature data with I<sup>2</sup>C. For the example, the 7-bit slave address is 0x6C.

Once the host controller (master) sends the start condition and data direction bit as READ (R/W=1), the master must send the first address byte of the register address (0x2E). Sensor starts transmitting the temperature (2 bytes) and pressure (2 bytes) data. Data from the

STATUS\_SYNC register is then transmitted in the following two bytes (5th and 6th). New temperature and pressure data is generated every 2.4 ms.

Figure 21 shows a random read and repeated read operation without the CRC.

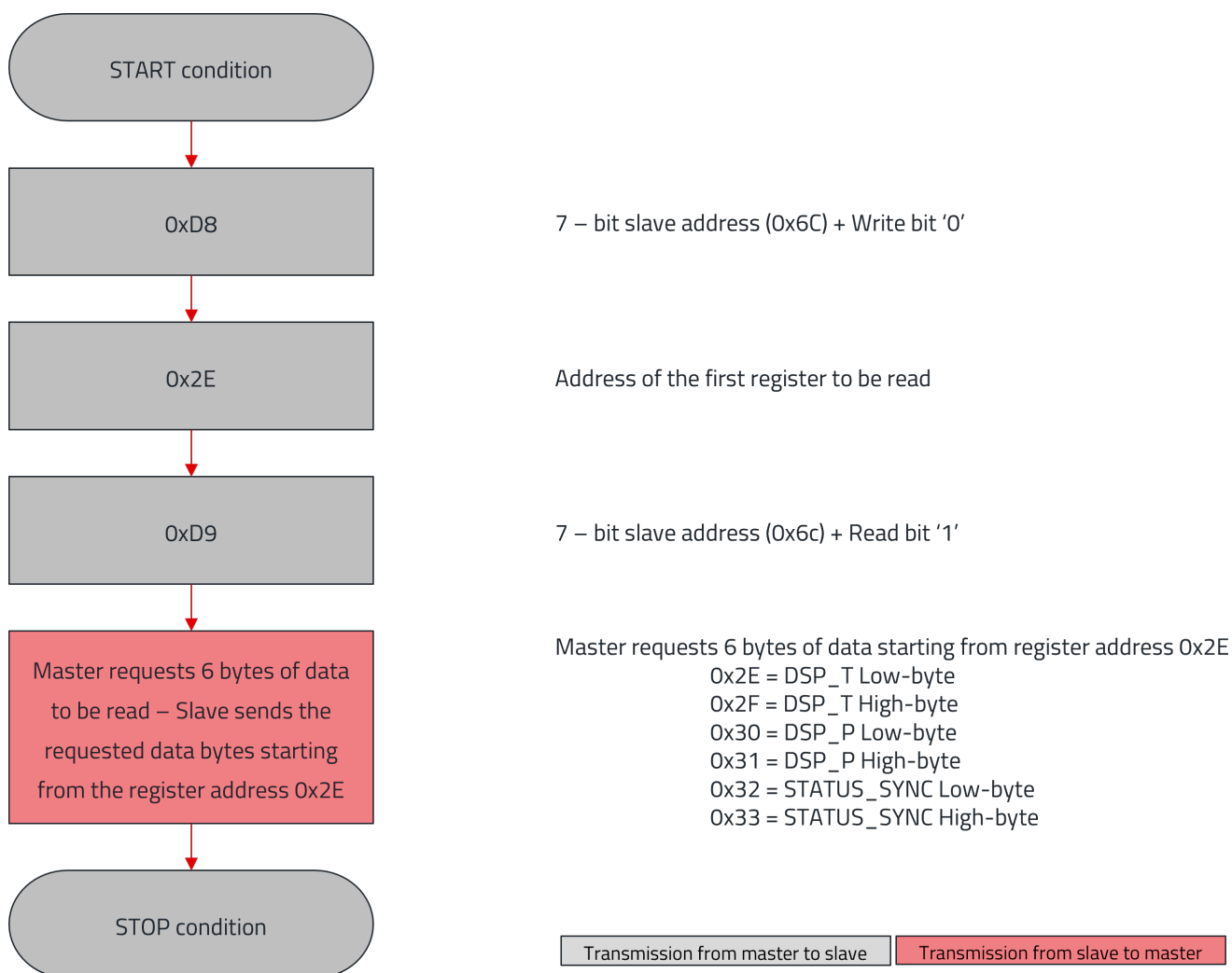


Figure 21: Reading output data with I<sup>2</sup>C interface



The sensor response time is about 2.4 ms, meaning it sends a new set of pressure (2 bytes) and temperature (2 bytes) data every 2.2 ms. With the I<sup>2</sup>C clock frequency of 400 kHz, the exchange of the 4 data bytes containing the current pressure and temperature values takes about 80 μs.



If it is not necessary to monitor every new pressure and temperature update, the STATUS\_SYNC flags can be ignored. In such cases, reading only two consecutive 16-bit words starting from address 0x2E (i.e., DSP\_T and DSP\_P) is sufficient.

### 8.3 Pressure output: digital

Part numbers: 2513130810105, 2513130810205, 2513130835205

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		16384		digits
Full scale span	FSS		26214		
Output at minimum pressure	OUT <sub>P_MIN</sub>		3277		
Output at maximum pressure	OUT <sub>P_MAX</sub>		29491		

Table 21: Digital pressure output: bidirectional

Part numbers: 2513130810305

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		3277		digits
Full scale span	FSS		26214		
Output at minimum pressure	OUT <sub>P_MIN</sub>		3277		
Output at maximum pressure	OUT <sub>P_MAX</sub>		29491		

Table 22: Digital pressure output: unidirectional

Part numbers: 2513130810405

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		5660		digits
Full scale span	FSS		26214		
Output at minimum pressure	OUT <sub>P_MIN</sub>		3277		
Output at maximum pressure	OUT <sub>P_MAX</sub>		29491		

Table 23: Digital pressure output



Digital output is not ratiometric to the positive supply voltage *VDD* .

8.4 Temperature output: digital

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Output at minimum temperature	OUT <sub>T_MIN</sub>		8192		digits
Output at maximum temperature	OUT <sub>T_MAX</sub>		24576		

Table 24: Digital temperature output: all devices

## 8.5 Interpreting digital pressure values

First two bytes transmitted from the sensor consists of pressure data where the first byte being most significant byte (MSB) and the second byte being least significant byte (LSB). The complete 16-bits pressure value can be obtained by concatenating the two bytes of pressure data. Corresponding pressure in SI unit (Pa) can be obtained from the digital pressure values with the help of sensitivity parameter  $SEN_P$  (see section 2.4.2).

Step 1: Get two bytes of pressure data

1.  $P_H$
2.  $P_L$

Step 2: Concatenate two pressure data bytes to obtain complete 16-bit pressure value

$$P_{16bits} = P_H \& P_L$$

Step 3: Obtain pressure value in SI unit (Pa) with following formula

$$\text{Pressure [kPa]} = [(P_{16bits} - OUT_{P\_MIN}) \times SEN_P] + P_{MIN}$$

Where,

- $P_{16bits}$  = Digital pressure value obtained in step 2. (in 2's complement signed 16-bit)
- $OUT_{P\_MIN}$  = Digital output at minimum pressure for the specific part number (see section: 8.3)
- $SEN_P$  = Sensitivity (digital) for specific part number (see section: 2.4.2)
- $P_{MIN}$  = Minimum pressure measurement range for specific part number (see section: 2.4.2)

### Example:

Pressure data obtained from sensor 2513130810305 (pressure range: 0 to 100 kPa) are:

$$P_H = 0x16$$

$$P_L = 0x1C$$

Concatenating these 2 bytes (0x161C) to obtain 16-bit decimal value

$$P_{16bits}[\text{digit}] = 5660$$

For part number 2513130810305,  $OUT_{P\_MIN} = 3277$  digits,  $SEN_P = 3.815 \times 10^{-3}$  kPa/digits and  $P_{MIN} = 0$  kPa.

$$P[\text{kPa}] = (5660 - 3277) [\text{digit}] \times 3.815 \times 10^{-3} [\text{kPa/digits}] = 9.09 \text{ kPa}$$

## 8.6 Interpreting digital temperature values

Following the pressure data, temperature data is transmitted as a 3rd and 4th byte. The complete 16-bits temperature value can be obtained by concatenating the two bytes of temperature data, where the 3rd byte being most significant byte (MSB) and the 4th byte being least significant byte (LSB) of the temperature value. Corresponding temperature in SI unit (°C) can be obtained from the digital temperature values with the help of sensitivity parameter  $SEN_T$  (see table 11).

Step 1: Get two bytes of temperature data

1.  $T_H$
2.  $T_L$

Step 2: Concatenate the two bytes to obtain complete 16-bit temperature value

$$T_{16bits} = T_H \& T_L$$

Step 3: Obtain temperature value in SI unit [°C] with following formula

$$\text{Temperature [°C]} = (T_{16bits} - OUT_{T\_MIN}) \times SEN_T$$

Where,

- $T_{16bits}$  = Digital temperature value obtained in step 2 (in 2's complement signed 16-bit)  
 $OUT_{T\_MIN}$  = Digital output at minimum temperature = 8192 [digit] (see table: 24)  
 $SEN_T$  =  $4.272 \times 10^{-3}$  [°C/digit] (see table: 11)

### Example:

Temperature data obtained from sensor are:

$$T_H = 0x36$$

$$T_L = 0xC5$$

Concatenating these 2 bytes (0x36C5) to obtain 16-bit decimal value

$$T_{16bits}[\text{digit}] = 14021$$

$$T[°C] = (14021 - 8192)[\text{digit}] \times 4.272 \times 10^{-3} [\text{digit}/°C] = 24.90 \text{ °C}$$

## 9 Reading analog pressure data

The sensors also produce fully calibrated pressure values as a ratiometric analog voltage output, which can be read through *VOUT* pin of the sensor. Following section shows the typical analog voltage values for the sensors.



Analog output voltage is ratiometric<sup>1</sup> to the positive supply voltage *VDD*.

### 9.1 Analog pressure output

**Part number: 2513130810105, 2513130810205, 2513130835205**

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		50		%V <sub>DD</sub>
Full scale span	FSS		80		
Output at minimum pressure	OUT <sub>P_MIN</sub>		10		
Output at maximum pressure	OUT <sub>P_MAX</sub>		90		

Table 25: Analog pressure output

**Part number: 2513130810305**

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		10		%V <sub>DD</sub>
Full scale span	FSS		80		
Output at minimum pressure	OUT <sub>P_MIN</sub>		10		
Output at maximum pressure	OUT <sub>P_MAX</sub>		90		

Table 26: Analog pressure output

<sup>1</sup> Ratiometric: Output signal changes in proportion to the change in supply voltage.

**Part number: 2513130810405**

Parameter	Symbol	Value			Unit
		Min	Typ	Max	
Zero pressure offset	OUT <sub>OFF</sub>		17.4		%V <sub>DD</sub>
Full scale span	FSS		80		
Output at minimum pressure	OUT <sub>P_MIN</sub>		10		
Output at maximum pressure	OUT <sub>P_MAX</sub>		90		

Table 27: Analog pressure output

## 10 Look-up Table

Partnumber	Operating Voltage (V <sub>DD</sub> )	OUT <sub>P_MIN</sub> (digits)	SEN <sub>P</sub> (kPa/digit)	P <sub>MIN</sub> (kPa)
2513130810105	3 to 5 V	3277	$7.63 \times 10^{-5}$	-1
2513130810205	3 to 5 V	3277	$7.63 \times 10^{-4}$	-10
2513130835205	3 to 5 V	3277	$2.67 \times 10^{-3}$	-35
2513130810305	3 to 5 V	3277	$3.815 \times 10^{-3}$	0
2513130810405	3 to 5 V	3277	$4.196 \times 10^{-2}$	-100

Table 28: look up table: Digital output

$$\text{Pressure [kPa]} = [(P_{15\text{bit}} - \text{OUT}_{P\_MIN}) \times \text{SEN}_P] + P_{MIN}$$

Partnumber	Operating Voltage (V <sub>DD</sub> )	OUT <sub>P_MIN</sub> (V)	SEN <sub>P</sub> (kPa/V)	P <sub>MIN</sub> (kPa)
2513130810105	3 to 5 V	10 %V <sub>DD</sub>	$(P_{MAX} - P_{MIN}) / 80 \%V_{DD}$	-1
2513130810205	3 to 5 V	10 %V <sub>DD</sub>	$(P_{MAX} - P_{MIN}) / 80 \%V_{DD}$	-10
2513130835205	3 to 5 V	10 %V <sub>DD</sub>	$(P_{MAX} - P_{MIN}) / 80 \%V_{DD}$	-35
2513130810305	3 to 5 V	10 %V <sub>DD</sub>	$(P_{MAX} - P_{MIN}) / 80 \%V_{DD}$	0
2513130810405	3 to 5 V	10 %V <sub>DD</sub>	$(P_{MAX} - P_{MIN}) / 80 \%V_{DD}$	-100

Table 29: look up table: Analog output

$$\text{Pressure [kPa]} = [(P_{\text{voltage}} - \text{OUT}_{P\_MIN}) \times \text{SEN}_P] + P_{MIN}$$

## 11 Physical specifications

### 11.1 Sensor drawing: Vertical Nozzles

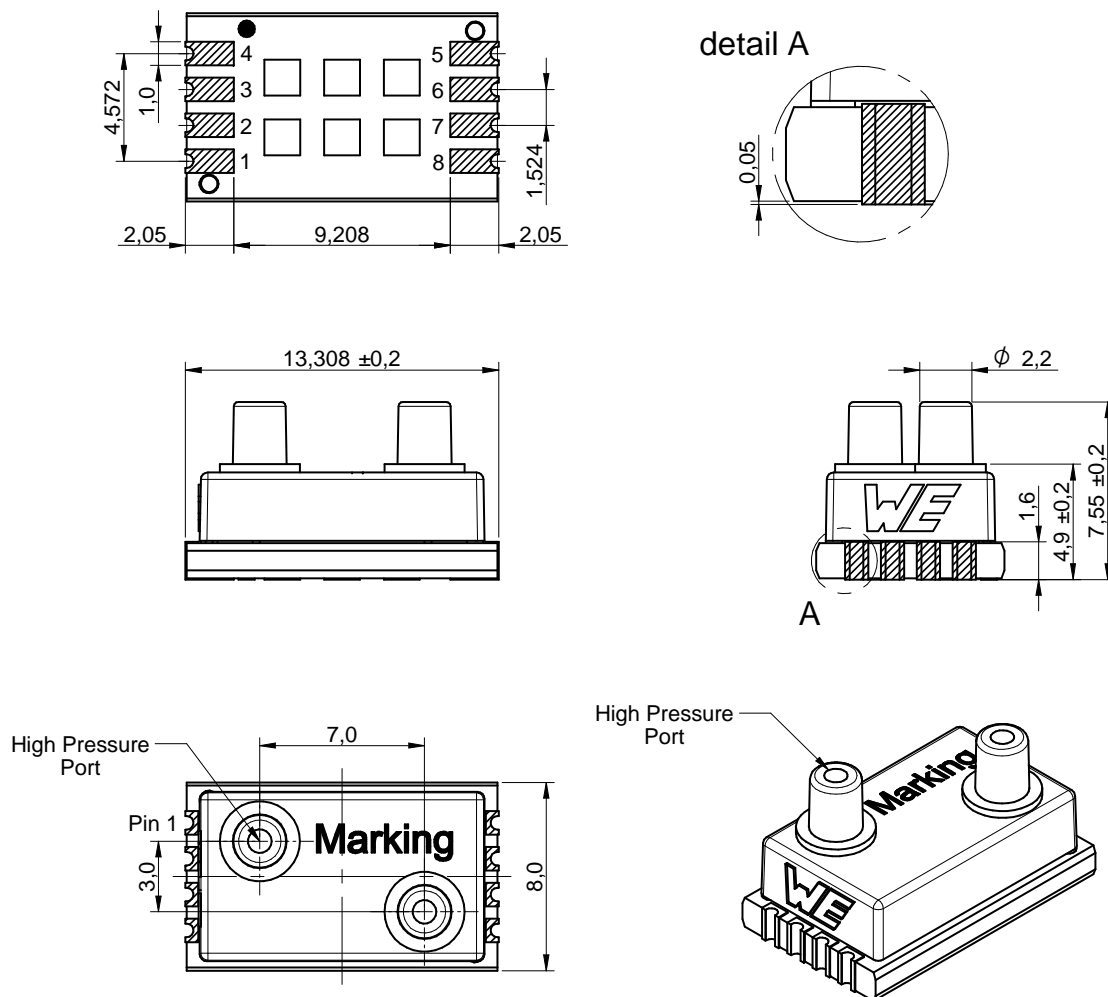


Figure 22: Sensor dimensions: Vertical nozzles [mm]

## 11.2 Sensor drawing: Horizontal-Barbed Nozzles

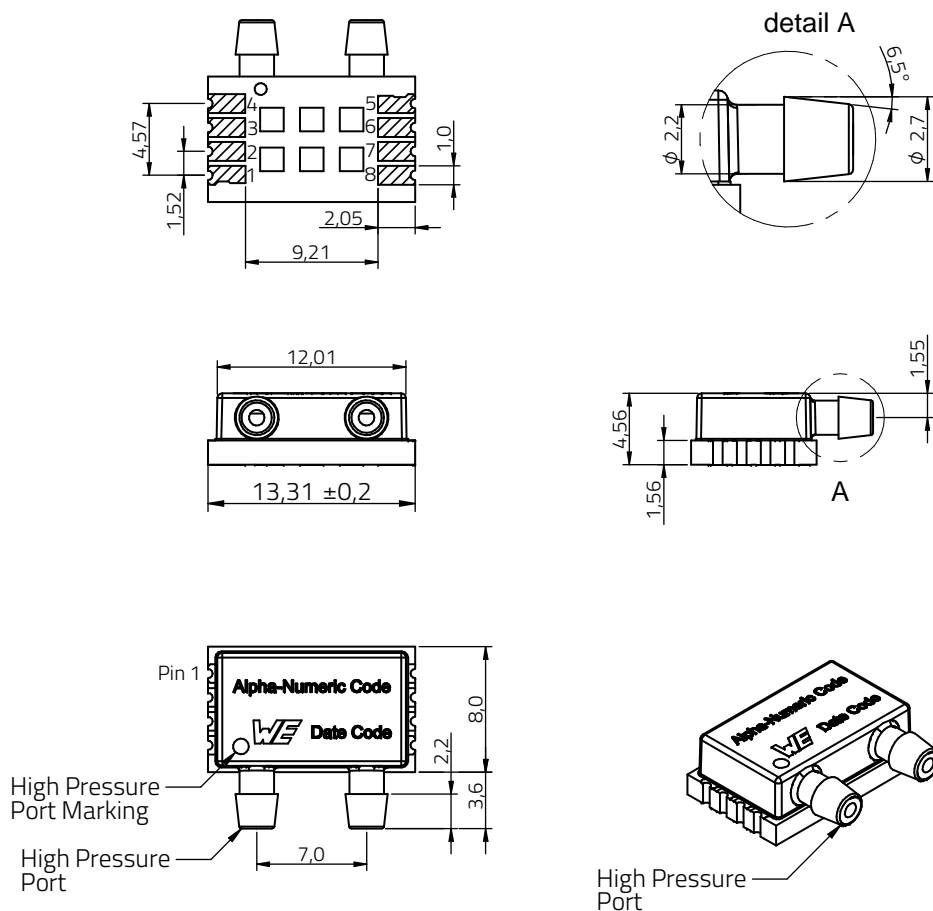


Figure 23: Sensor dimensions: Horizontal-barbed nozzles [mm]

## 11.3 Footprint

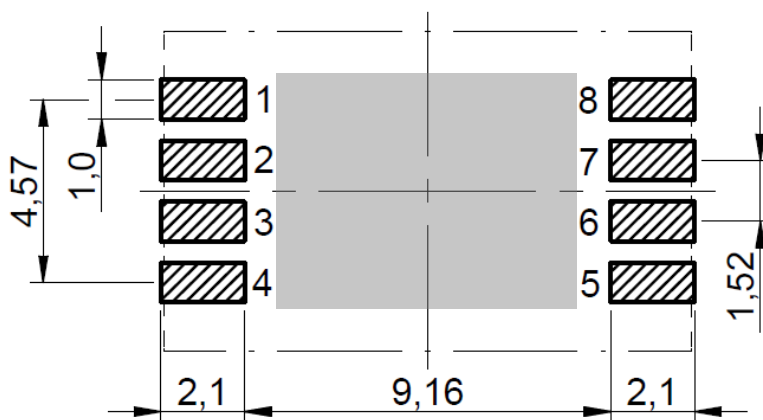


Figure 24: Recommended land pattern [mm] (top view)



Open traces, open wires or vias are not allowed in the centre area of the sensor (marked in grey in the figure 24)

## 11.4 Marking information

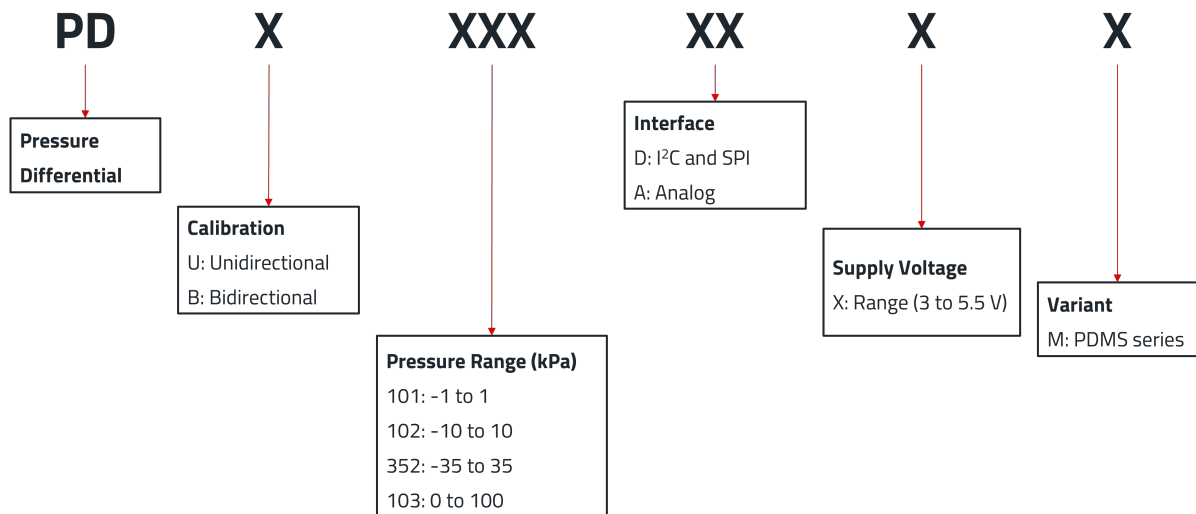


Figure 25: Marking information



Marking PDB101DAXM indicates a differential pressure sensor with measurement range from -1 to 1 kPa. The corresponding WE part number is 2513130810101.

## 12 Manufacturing information

### 12.1 Moisture sensitivity level

The sensor product is categorized as JEDEC Moisture Sensitivity Level 1 (MSL1), which requires special handling.

More information regarding the MSL requirements can be found in the IPC/JEDEC J-STD-020 standard on [www.jedec.org](http://www.jedec.org). More information about the handling, picking, shipping and the usage of moisture/re-flow and/or process sensitive products can be found in the IPC/JEDEC J-STD-033 standard on [www.jedec.org](http://www.jedec.org).

### 12.2 Reflow soldering

Attention must be paid on the thickness of the solder resist between the host PCB top side and the modules bottom side. Only lead-free assembly is recommended according to JEDEC J-STD020.

Profile feature		Value
Preheat temperature, min	$T_{S \text{ Min}}$	150 °C
Preheat temperature, max	$T_{S \text{ Max}}$	200 °C
Preheat time from $T_{S \text{ Min}}$ to $T_{S \text{ Max}}$	$t_S$	60 - 120 s
Ramp-up rate ( $T_L$ to $T_P$ )		3 °C/s max.
Liquidous temperature	$T_L$	217 °C
Time $t_L$ maintained above $T_L$	$t_L$	60 - 150 s
Peak package body temperature	$T_P$	see table below
Time within 5 °C of actual peak temperature	$t_P$	20 - 30 s
Ramp-down rate ( $T_P$ to $T_L$ )		6 °C/s max.
Time 20 °C to $T_P$		8 min max.

Table 30: Classification reflow soldering profile, Note: refer to IPC/JEDEC J-STD-020E



In order to reduce residual stress on the sensor component, the recommended ramp-down temperature slope should be lower than 3 °C/s.

Package thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
< 1.6 mm	260 °C	260 °C	260 °C
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C
> 2.5 mm	250 °C	245 °C	245 °C

Table 31: Package classification reflow temperature, PB-free assembly, Note: refer to IPC/-JEDEC J-STD-020E

It is recommended to solder the sensor on the last re-flow cycle of the PCB. For solder paste use a LFM-48W or Indium based SAC 305 alloy (Sn 96.5 / Ag 3.0 / Cu 0.5 / Indium 8.9HF / Type 3 / 89 %) type 3 or higher.

The reflow profile must be adjusted based on the thermal mass of the entire populated PCB, heat transfer efficiency of the reflow oven and the specific type of solder paste used. Based on the specific process and PCB layout the optimal soldering profile must be adjusted and verified. Other soldering methods (e.g. vapor phase) have not been verified and have to be validated by the customer at their own risk. Rework is not recommended.

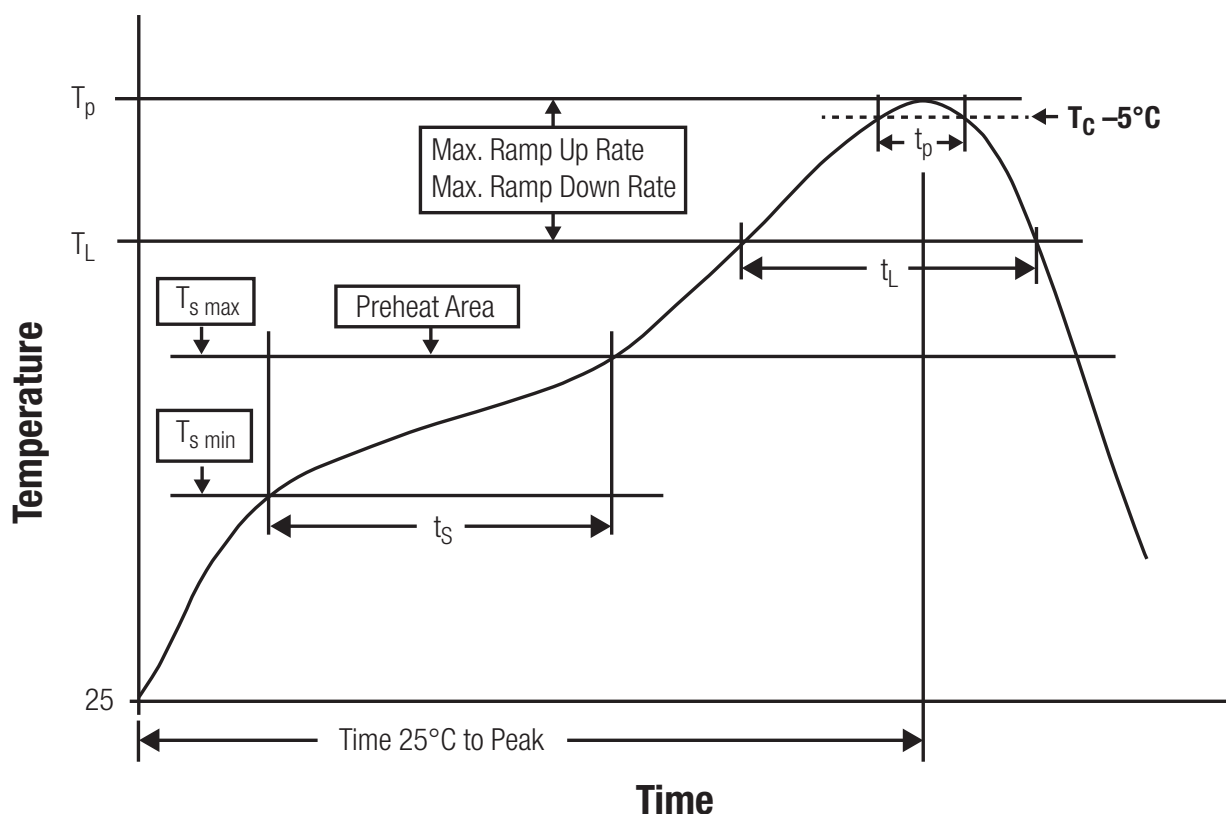


Figure 26: Reflow soldering profile

After reflow soldering, visually inspect the board to confirm proper alignment.

## 12.3 Cleaning and washing

- Do not clean the product. Any residue cannot be easily removed by washing. Use a "no clean" soldering paste and do not clean the board after soldering.
- Washing agents used during the production to clean the customer application might damage or change the characteristics of the component. Washing agents may have a negative effect on the long-term functionality of the product.
- Using a brush during the cleaning process may damage the component. Therefore, we do not recommend using a brush during the PCB cleaning process

## 12.4 Potting and coating

- Potting material might shrink or expand during and after hardening. This might apply mechanical stress on the components, which can influence the characteristics of the transfer function. In addition, potting material can close existing openings in the housing. This can lead to a malfunction of the component. Thus, potting is not recommended.
- Conformal coating may affect the product performance. We do not recommend coating the components.

## 12.5 Storage conditions

- A storage of Würth Elektronik eiSos products for longer than 12 months is not recommended. Within other effects, the terminals may suffer degradation, resulting in bad solderability. Therefore, all products shall be used within the period of 12 months based on the day of shipment.
- Do not expose the components to direct sunlight.
- The storage conditions in the original packaging are defined according to DIN EN 61760 - 2.
- For a moisture sensitive component, the storage condition in the original packaging is defined according to IPC/JEDEC-J-STD-033. It is also recommended to return the component to the original moisture proof bag and reseal the moisture proof bag again.

## 12.6 Handling

- Violation of the technical product specifications such as exceeding the nominal rated supply voltage, will void the warranty.
- Violation of the technical product specifications such as but not limited to exceeding the absolute maximum ratings will void the conformance to regulatory requirements.
- ESD prevention methods need to be followed for manual handling and processing by machinery.
- The edge castellation is designed and made for prototyping, i.e. hand soldering purposes only.

- The applicable country regulations and specific environmental regulations must be observed.
- Do not disassemble the product. Evidence of tampering will void the warranty.

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